



ACE25QC800G

8M BIT SPI NOR FLASH

Description

The ACE25QC800G is 8M-bit Serial Peripheral Interface(SPI) Flash memory, and supports the Dual/Quad SPI: Serial Clock, Chip Select, Serial Data I/O0 (SI), I/O1 (SO), I/O2 (/WP), and I/O3 (/HOLD). The Dual I/O data is transferred with speed of 240Mbits/s and the Quad I/O & Quad output data is transferred with speed of 480Mbits/s. The device uses a single low voltage power supply, ranging from 2.7 Volt to 3.6 Volt.

Additionally, the device supports JEDEC standard manufacturer and device ID and three 256-bytes Security Registers.

In order to meet environmental requirements, offers 8-pin SOP, 8-pin SOP 208mil, 8-pad WSON 6x5-mm, 8-pin 3*2 USON ◦

Features

- Serial Peripheral Interface (SPI)
 - Standard SPI: SCLK, /CS, SI, SO, /WP, /HOLD
 - Dual SPI: SCLK, /CS, IO0, IO1, /WP, /HOLD
 - Quad SPI: SCLK, /CS, IO0, IO1, IO2, IO3
- Read
 - Normal Read (Serial): 55MHz clock rate
 - Fast Read (Serial): 108MHz clock rate with 30PF load
 - Dual I/O data transfer up to 216Mbits/S
 - Quad I/O data transfer up to 432Mbits/S
 - Continuous Read with 8/16/32/64-byte Wrap
- Program
 - Serial-input Page Program up to 256bytes
 - Program Suspend and Resume
- Erase
 - Block erase (64/32 KB)
 - Sector erase (4 KB)
 - Chip erase
 - Erase Suspend and Resume
- Program/Erase Speed
 - Page Program time: 0.6ms typical
 - Sector Erase time: 50ms typical
 - Block Erase time: 0.15/0.25s typical
 - Chip Erase time: 4s typical
- Flexible Architecture
 - Sector of 4K-byte
 - Block of 32/64K-byte



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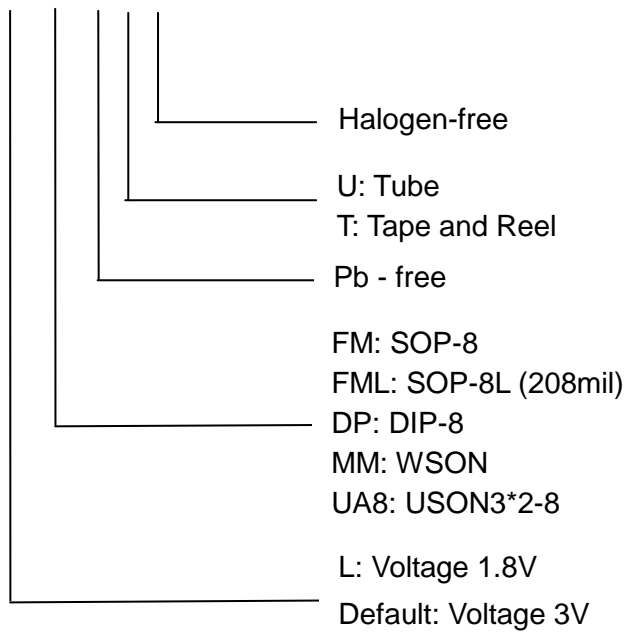
- **Low Power Consumption**
 - 20mA maximum active current
 - 5uA maximum power down current
- **Software/Hardware Write Protection**
 - 3x256-Byte Security Registers with OTP Locks
 - Discoverable Parameters (SFDP) register
 - Enable/Disable protection with WP Pin
 - Write protect all/portion of memory via software
 - Top or Bottom, Sector or Block selection
- **Single Supply Voltage**
 - Full voltage range: 2.7~3.6V
- **Temperature Range**
 - Industrial (-40°C to 85°C)
- **Cycling Endurance/Data Retention**
 - Typical 100k Program-Erase cycles on any sector
 - Typical 20-year data retention at 55°C



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Ordering information

ACE25QC800GX XXX + X H





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Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD. As sued herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.